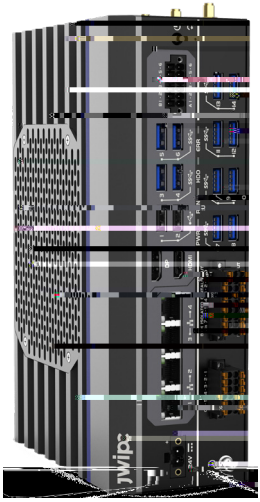
PAS-7513



Version 1.0

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3.1

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3.2 PAS-7513

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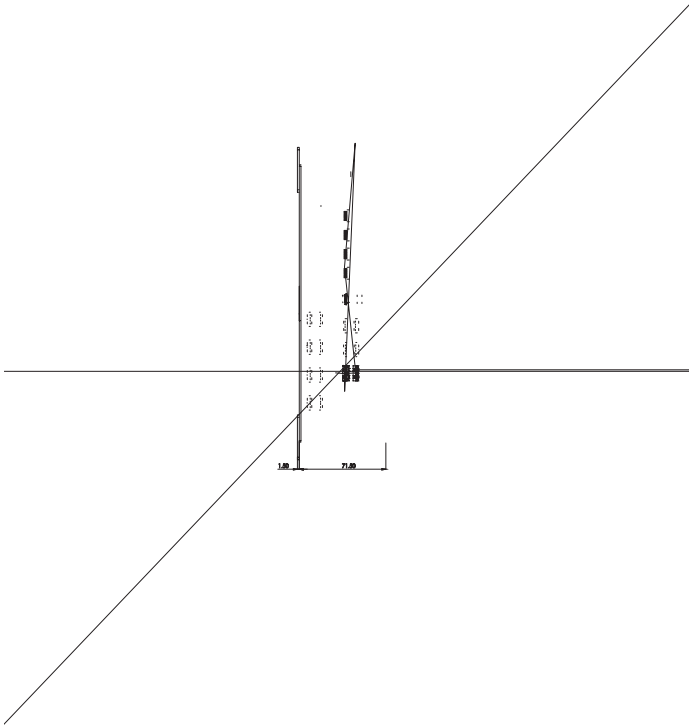
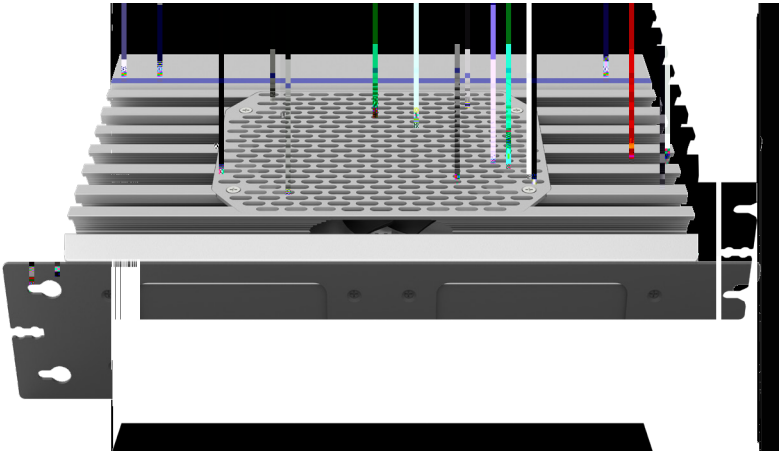
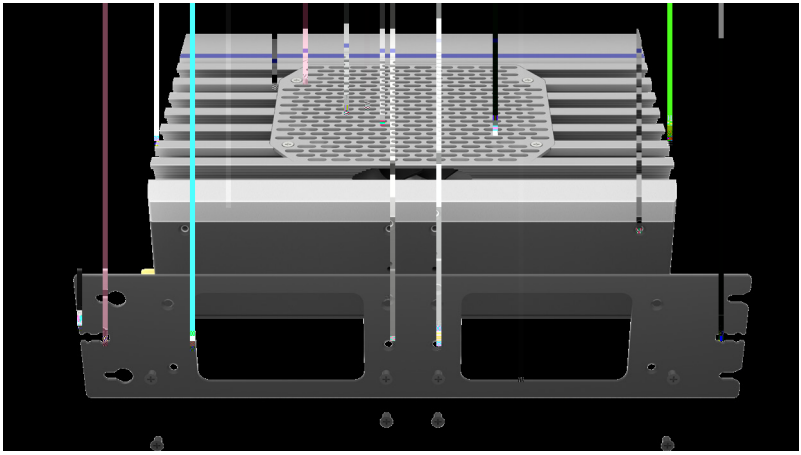


3.3 PAS-7513





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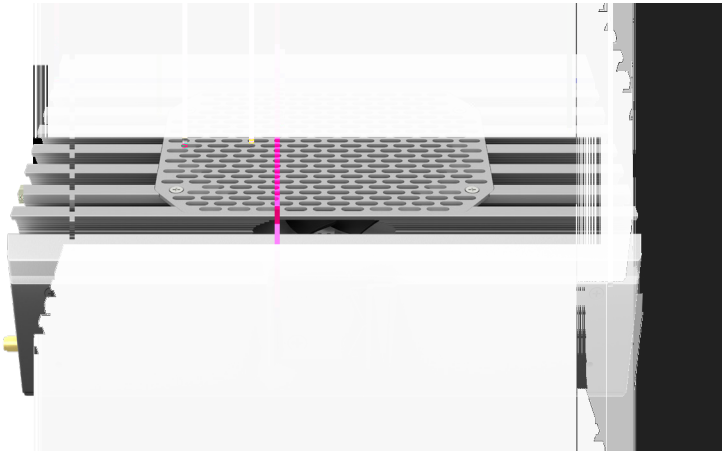
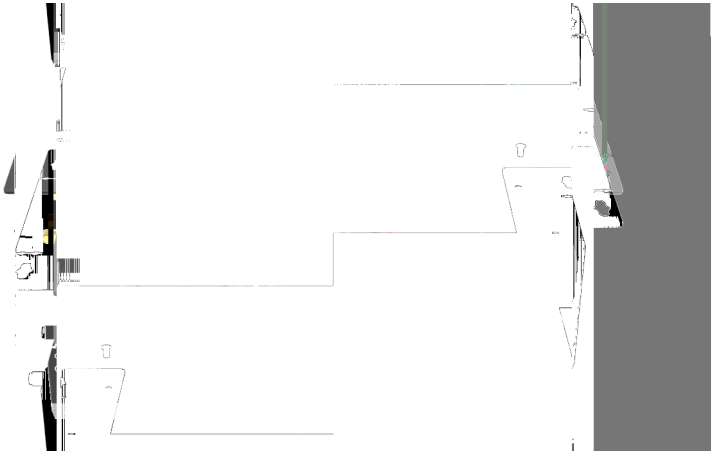
3.4 PAS-7513

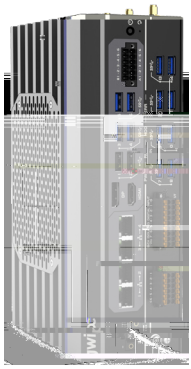


3.5 PAS-7513

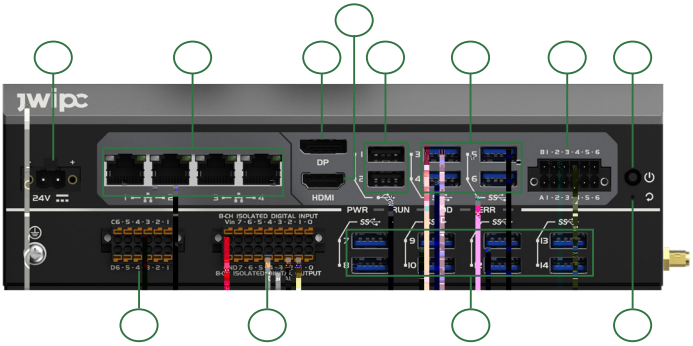


5

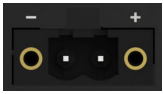
5.1 PAS-7513



5.2 PAS-7513 IO



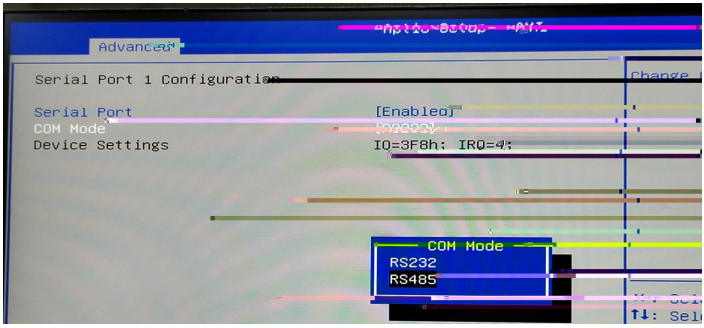
5.3

		
	.	AC*
	/	AC(

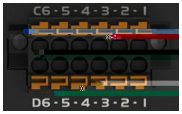
5.4 COM_CAN

	PIN	RS232	RS422	RS485	CAN	
	>.	CLM._ACA	/	CLM._152*	/	/
	>/	CLM._PFN	/	CLM._152(	/	/
	>0	CLM._PLRT	/	/	/	/
	>1	/	/	/	/	DNA
	>2	/	/	/	/	DNA
	>3	/	/	/	/	MTOBTN
	B.	CLM/_ACA	/	CLM/_152*	/	/
	B/	CLM/_PFN	/	CLM/_152(	/	/
	B0	CLM/_PLRT	/	/	/	/
	B1	/	/	/	DNA	/
	B2	/	/	/	C>N._E	/
	B3	/	/	/	C>N._I	/

ч CLM. / / OP/0/ / 152 h BFLP h ч



5.5 COM3_4



PIN	RS232	RS422	RS485	
C.	CLM0_ACA	CLM0_1//TU*	CLM0_152*	/
C/	CLM0_PFN	CLM0_1//TU(	CLM0_152(	/
C0	CLM0_PLRT	CLM0_1//OU(	/	/
C1	CLM0_ATO	CLM0_1//OU*	/	/
C2	/	/	/	DNA
C3	/	/	/	/
A.	CLM1_ACA	CLM1_1//TU*	CLM1_152*	/
A/	CLM1_PFN	CLM1_1//TU(	CLM1_152(	/
A0	CLM1_PLRT	CLM1_1//OU(	/	/
A1	CLM1_ATO	CLM1_1//OU*	/	/
A2	/	/	/	DNA
A3	/	/	/	/

ч CLM0 / 1 OP/0/ / OP1/ / / OP152 ч h ч

RS232	RS485	RS422
GC. (.*/&	GC. (0*1&	GC. (2*3&
GC/ (.*0&	GC/ (0*2&	GC/ (0*2&
GC/ (/*1&	GC/ (1*3&	GC/ (1*3&
GC0 (.*0&	GC0 (0*2&	GC0 (0*2&
GC0 (/*1&	GC0 (1*3&	GC0 (1*3&

RS232	RS485	RS422
GC1 (.*/&	GC1 (0*1&	GC1 (2*3&
GC2 (.*0&	GC2 (0*2&	GC2 (0*2&
GC2 (/*1&	GC2 (1*3&	GC2 (1*3&
GC3 (.*0&	GC0 (0*2&	GC3 (0*2&
GC3 (/*1&	GC0 (1*3&	GC3 (1*3&

5.7

5.7.1 ATX-M1

.*/	>TU MIab
/*0	>T MIab

5.7.2 FAN

.	DNA
/	(2S
0	MTM_FN
1	MTM_LRT

5.7.3 BAT

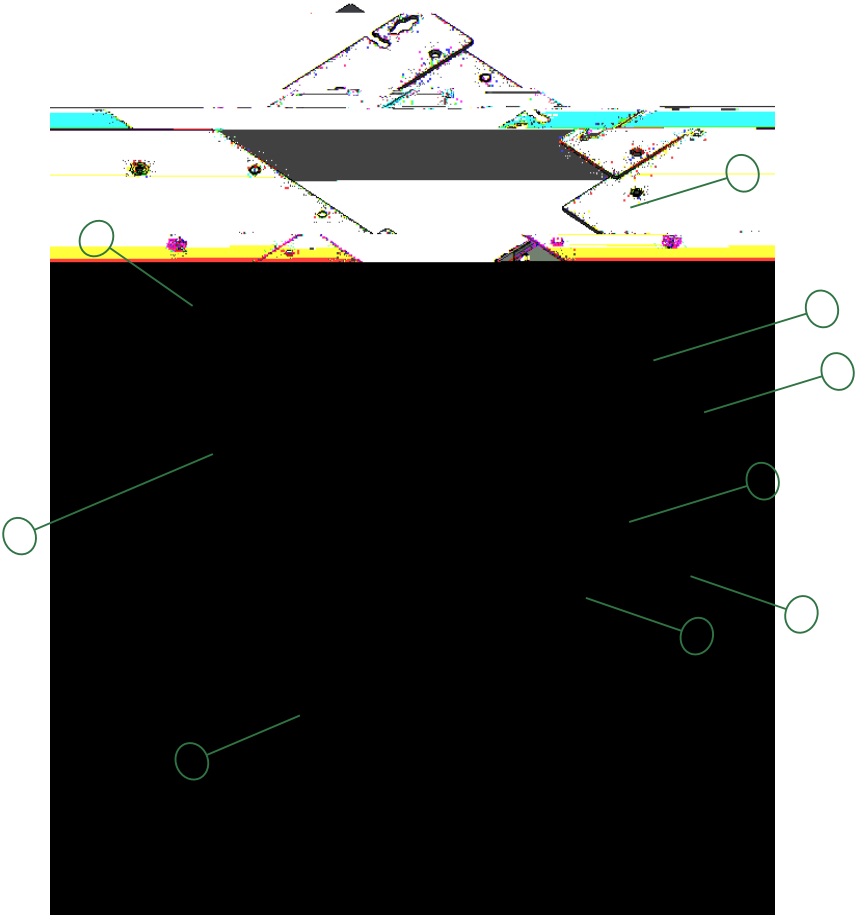
.	B>T(
/	B>T*

5.7.4 PWRBTN

.	(
/	DNA

6

6.1 PAS-7513



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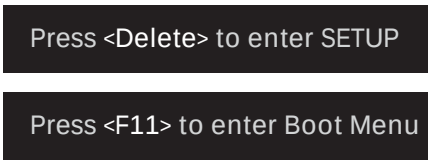
7 BIOS

7.1 BIOS

7.2 BIOS

7.2.1 BIOS



7.2.2

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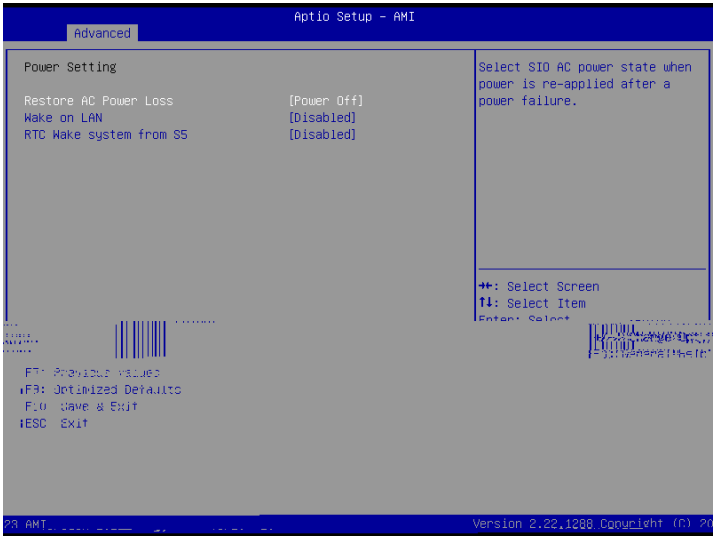
↑ / ↓	
+/ -	

7.5 Advanced

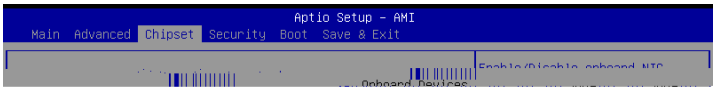
Trusted Computing		
Hardware Monitor		
IT8786 Super IO Con f guration	Prmbo FL	
CPU Con f guration	C MR	
Power Settings		

Serial Port 1/2/3/4/5/6 Configuration

- Mltbo Clk ž dro a t flk 9Bk t bo > G



7.6 Chipset



PCH LAN Controller(I219)

G

ч Bk a _iba)Afp a _iba .

I226_1/2/3

G

ч Afp a _ib)Bk a _ib .

Lan BootROM

OLM G

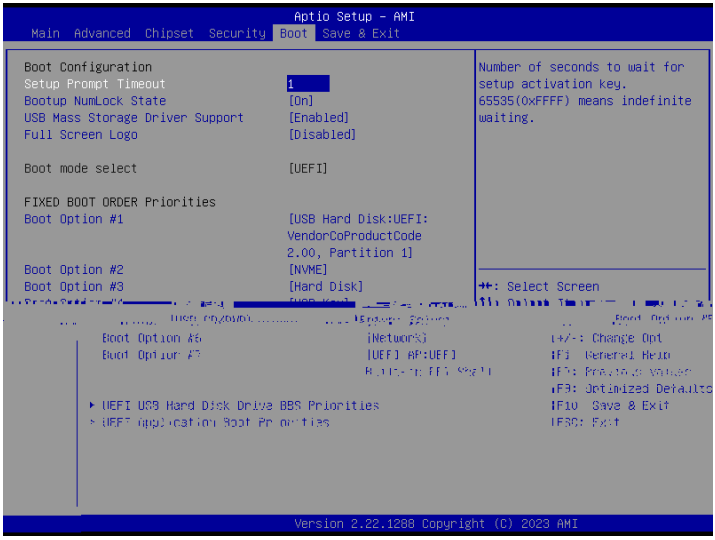
ч Afp a _ib)Bk a _ib .

Primary Display

Hυ G

ч >r t l)Bk a _iba)Afp a _iba .

7.8 Boot



Boot NumLock State

N r m Il c h G

ч Lk)L ƀ .

USB Mass Storage Driver Support

RP B G

ч Afp a _ib)Bk a _ib .

Full Screen Logo

Ildl G

ч Afp a _ib)Bk a _ib .

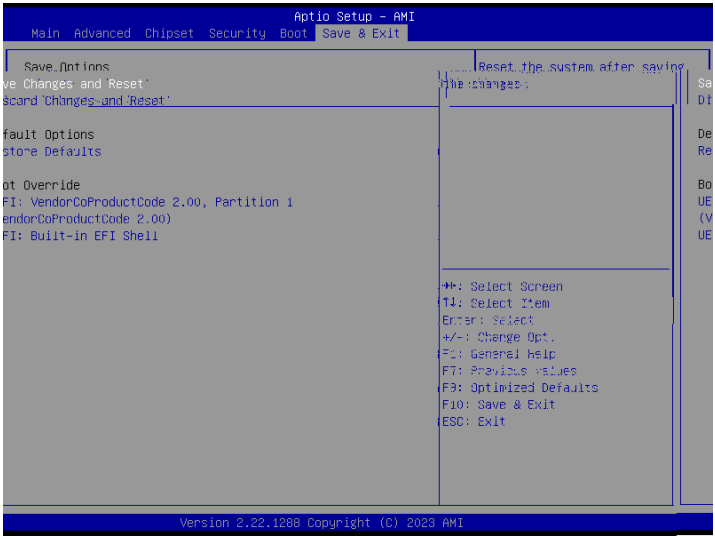
Boot Option #1-7

G

ч N SMB)E a oa Afph)RP B E a oa Afph)RP B hbv)RP B C A / ASA) N b t tloh)RI

B rfi t *fk BCF Pebii .

7.9 Save&Exit



Save Changes and Reset

G

Discard Changes and Reset

G

Restore Defaults

G

8

8.1 WDT

TAT PFL IAN ABS4) -U4/ BFT4 . h -
-UC0 h -U4/ BFT4 . h -UC0 -U/- 0/

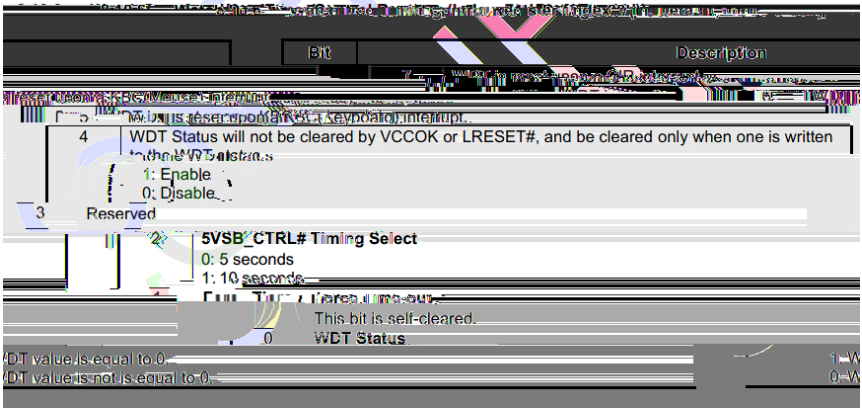
8.1.1

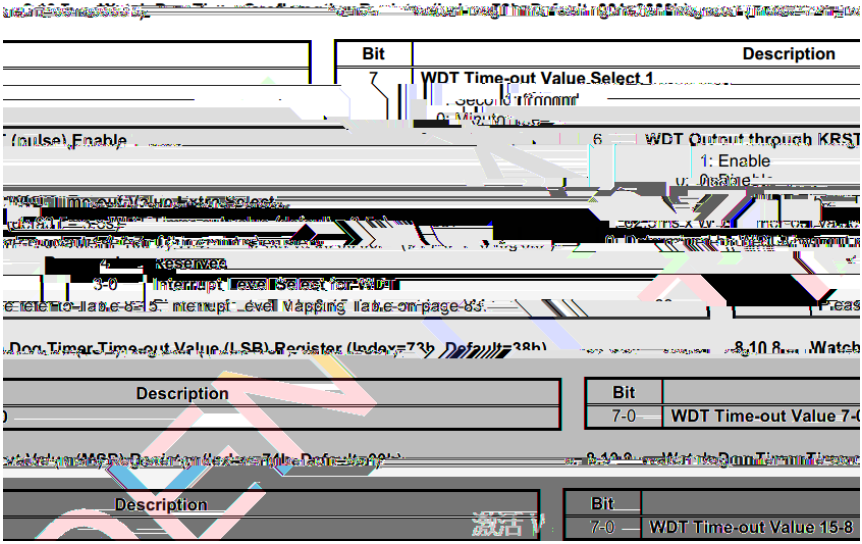
```
    / / bk t bo  f t b  c lk ž d
  FlTof t b5 ( PflFkabuMlo t )-u54&8
  FlTof t b5 ( PflFkabuMlo t )-u-.&8
  FlTof t b5 ( PflFkabuMlo t )-u22&8
  FlTof t b5 ( PflFkabuMlo t )-u22&8

    / / pbib c t  ta t c eald  absf c b
  FlTof t b5 ( PflFkabuMlo t )-u-4&8
  FlTof t b5 ( PflA a t  a Mlo t )-u-4&8
  FlTof t b5 ( PflFkabuMlo t )  -uC.&8
   t bmm _ a a t a  =  FlOb a a5 ( PflA a t  a Mlo t &8
   t bmm _ a a t a  #=  -u B B8
   t bmm _ a a t a  y=  -u118
  FlTof t b5 ( PflFkabuMlo t )  -uC.&8
  FlTof t b5 ( PflA a t  a Mlo t )  t bmm _ a a t a&8

    / / M>M  t l  DM.-
  FlTof t b5 ( PflFkabuMlo t )  -uC1&8
   t bmm _ a a t a  =  FlOb a a5 ( PflA a t  a Mlo t &8
   t bmm _ a a t a  #=  -uC-8
   t bmm _ a a t a  y=  -u-58
  FlTof t b5 ( PflFkabuMlo t )  -uC1&8
    / / ta t  p t a o t
    fc ( f t b _ t a t  c e _ ald _ bibmbk t *> ta t _ lmbo a t lo = = PT>OT _ TAT &x

    / / c ib a o  p t a t rp
    FlTof t b5 ( PflFkabuMlo t )  -u4.&8
     t bmm _ a a t a  =  FlOb a a5 ( PflA a t  a Mlo t &8
     t bmm _ a a t a  #=  -ucb8
    FlTof t b5 ( PflFkabuMlo t )  -u4.&8
    FlTof t b5 ( PflA a t  a Mlo t )  t bmm _ a a t a&8

FlTof t b5 ( PflFkabuMlo t )  -u4/&8
 t bmm _ a a t a  =  FlOb a a5 ( PflA a t  a Mlo t &8

 t bmm _ a a t a  #=  -u.c8
 t bmm _ a a t a  y=  -uC-8
 / / mfkr t b  lo  pb c lka  BFT4 .*> pb c lka  -*> mfkr t b
fc ( f t b _ t a t  c e _ ald _ bibmbk t *> ta t _ rkf t  = =  .&x
 t bmm _ a a t a  #=  -u4c8
 }

FlTof t b5 ( PflFkabuMlo t )  -u4/&8
FlTof t b5 ( PflA a t  a Mlo t )  t bmm _ a a t a&8
```

```
        / / Cibao  P t a t rp
        FITof t b5 ( PflFkabuMlo t )  -u4.&8
         t bmm_a a t a  =  FlOb a a5 ( PflA a t a Mlo t &8
         t bmm_a a t a  #=  -ucb8
        FITof t b5 ( PflFkabuMlo t )  -u4.&8
        FITof t b5 ( PflA a t a Mlo t )  t bmm_a a t a&8

        / / Clrk t
        FITof t b5 ( PflFkabuMlo t )  -u41&8
        FITof t b5 ( PflA a t a Mlo t )  -u-&8
        FITof t b5 ( PflFkabuMlo t )  -u40&8
        FITof t b5 ( PflA a t a Mlo t )  f t b_t a t c e_ald_bibmbk t *>ta t _ t fmb&8
                    / / mofk t c ( "ta t  p t a o t 7%uYk ")FlOb a a5 ( a a t a&&8
    }
```

8.1.2

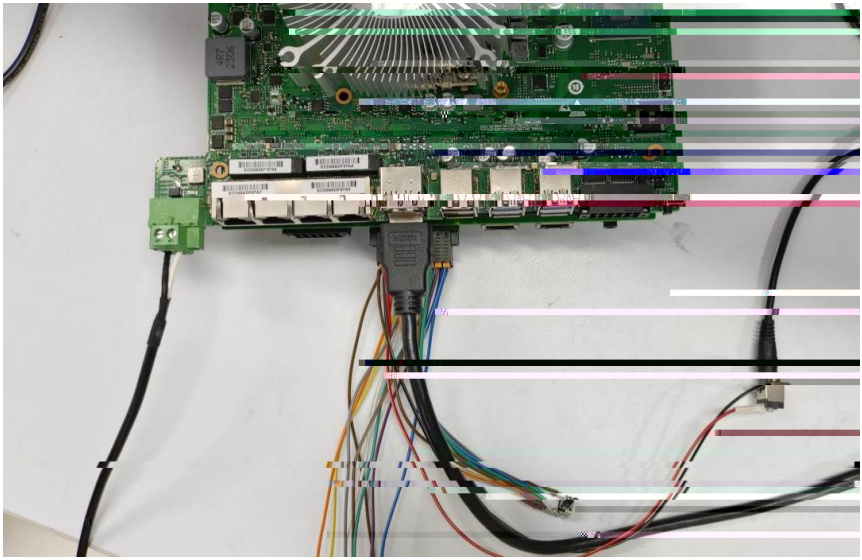
```
        / / ta t  p t lm
        fc ( f t b_t a t c e_ald_bibmbk t *>ta t _lmbo a t lo= =PTLM_TAT&x
                FITof t b5 ( PflFkabuMlo t )  -u41&8
                FITof t b5 ( PflA a t a Mlo t )  -u-&8
                FITof t b5 ( PflFkabuMlo t )  -u40&8
                FITof t b5 ( PflA a t a Mlo t )  -u-&8
            }
```



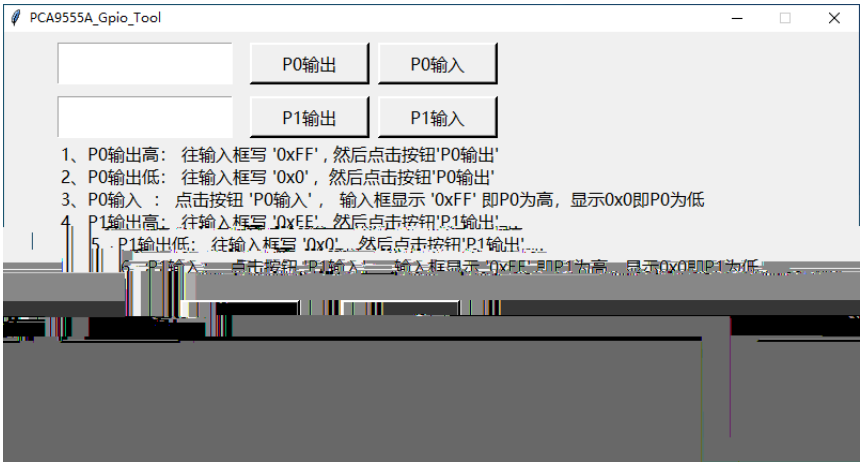


8.2 GPIO

.Hu h g 2S ŸIBA ÿ g Ÿ2S ./S /1S ÿ



/Hυ MC>6222>_Dmfl_ Tlli



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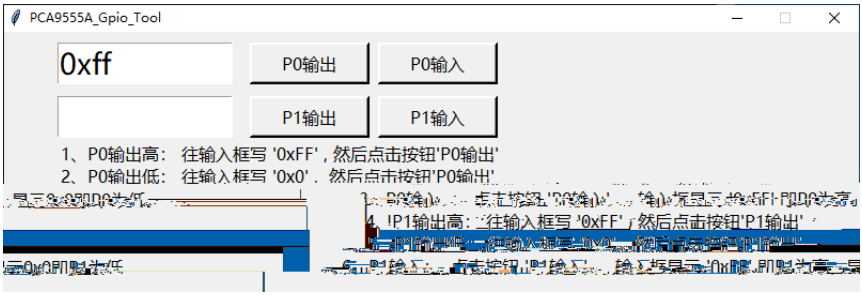

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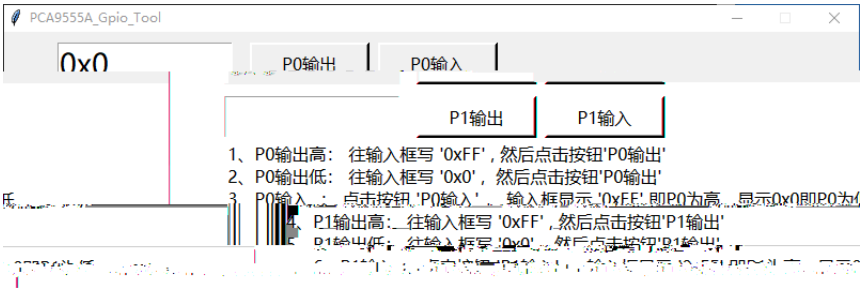
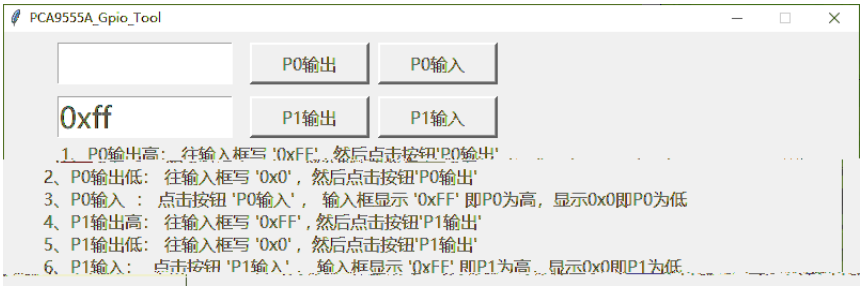
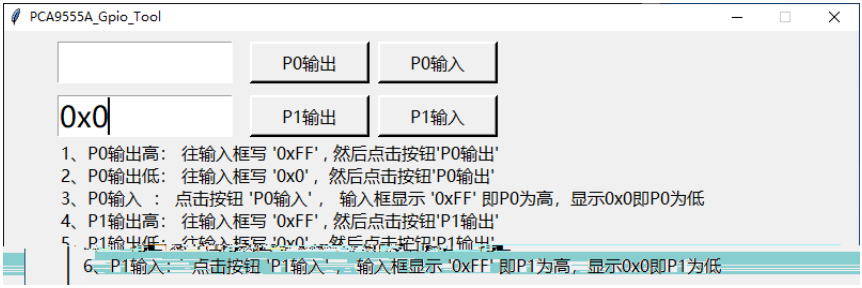


2Ҥ ɗM. d h -uƀ Ÿ ɗM. d h -uƀ h ɗM. d ɢ ÿ
h ɢ



3Ҥ ɗM. d h -u- h ɗM. d ɢ h ɢ



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		ɢ h
		.Ң /Ң 0Ң h
		ÿ ÿ Ң
		ÿ Tfkmb ÿ h h
FҢL h		MCF FP> Ң Ң h
	BFLP	h BFLP Pb t rm h BFLP Pb t rm
BFLP h		
Bll t absf c b kl t h clrka	BFLP h	BFLP ԁ d (Bll t & h
RPB	RPB / . - h RPB / . -	RPB / . -
	RPB	.Ң RPB lbd a c v Prmmlo t ÿ RPB ÿ /Ң h RPB